

Parallel Programming

Q: So lets just all use multicore from now on!

A: Software must be written as parallel program

Multicore difficulties

- Partitioning work
- **Coordination & synchronization**
- Communications overhead **HW**
- How do you write parallel programs?
... without knowing exact underlying architecture?

SW
Your
career...

Synchronization Of Parallel Programs

Jean-Loup Baer, Richard N. Zucker



Synchronization Of Parallel Programs:

Cas075 Françoise André, Daniel Herman, J.-P. Verjus, 1985 This book proposes a method for expressing typical cooperation and competition problems as well as different synchronization and communication methods for implementing them *Distributed Synchronization of Parallel Programs* Patrice Quinton, Jean-Pierre Verjus, 1986 **Compiler Analysis to Implement Point-to-point Synchronization in Parallel Programs** John Nguyen, 1993 Abstract The shared memory data parallel model presents an attractive interface for programming multiprocessors by allowing for easy management of parallel tasks while hiding details of the underlying machine architecture Unfortunately the shared memory abstraction requires synchronization in order to maintain data consistency Present compilers provide consistency between parallel code sections by enforcing a global point of synchrony with a barrier synchronization Such a simple mechanism possesses several disadvantages First the required global collection of information generates significant overhead which leads machine designers to employ special hardware to support barriers Second global synchronization reduces parallelism by requiring needless serialization of independent tasks This work aims to reduce the costs associated with these disadvantages by generating pairwise point to point synchronization between specific tasks Implementation of point to point synchronization demands extensive analysis of program dependences A compiler must perform flow analysis and dependence testing in order to compute lexical dependences between program statements In addition dynamic dependences between processors must be computed by examining array reference and statement contexts The final synchronization scheme must support any dependences that arise in the program while ensuring that no deadlock scenarios can occur This work proposes algorithms that satisfy such requirements and presents some encouraging results from a preliminary implementation **Pro .NET 4 Parallel Programming in C#** Adam Freeman, 2011-01-11 Parallel programming has been revolutionised in NET 4 providing for the first time a standardised and simplified method for creating robust scalable and reliable multi threaded applications The Parallel programming features of NET 4 allow the programmer to create applications that harness the power of multi core and multi processor machines Simpler to use and more powerful than classic NET threads parallel programming allows the developer to remain focused on the work an application needs to perform In Pro NET 4 Parallel Programming in C Adam Freeman presents expert advice that guides you through the process of creating concurrent C applications from the ground up You ll be introduced to NET s parallel programming features both old and new discover the key functionality that has been introduced in NET 4 and learn how you can take advantage of the power of multi core and multi processor machines with ease Pro NET4 Parallel Programming in C is a reliable companion that will remain with you as you explore the parallel programming universe elegantly and comprehensively explaining all aspects of parallel programming guiding you around potential pitfalls and providing clear cut solutions to the common problems that you will encounter **Modelling Communication and Synchronization in Parallel Programming Languages** Dennis Harold Freidel, 1984 *Static*

Analyses and Optimizations for Parallel Programs with Synchronization Yuan Zhang, 2008 As parallel systems become dominant in computer architectures parallel programs that take the advantage of multiple threads of control are taking the place of conventional sequential programs Yet parallel programs have introduced new challenges to compiler static analyses and optimizations On one hand classical compiler analyses and optimizations need to be enhanced to handle the inter thread interference and synchronization enforced dependence relation in a parallel program On the other hand new analyses and optimizations need to be designed for new programming language features In this dissertation we propose the following compiler static analyses and optimizations for parallel programs with synchronization

- 1 An interprocedural multi valued expressions analysis for both distributed and shared memory programs based on program slicing An expression is multi valued if it evaluates differently in different threads For Single Program Multiple Data style programs multi valued expressions analysis is a key component to determine the concurrent paths which are executed by different threads concurrently
- 2 An interprocedural barrier matching technique for both distributed and shared memory programs to detect synchronization errors caused by the misplacement of barriers or if the program is validated to determine the set of barrier statements that synchronize together
- 3 The first interprocedural concurrency analysis algorithm that can handle shared memory programs with textually unaligned barriers
- 4 A new framework of flow sensitive and synchronization sensitive data flow analyses for shared memory programs Pointer analysis is used as an example to demonstrate the effectiveness of this framework The most significant advantage of this framework is that inter thread interference and synchronization enforced dependence are modeled as concurrency relation thus it is easily extensible to new language features and compatible with different memory models
- 5 A lock assignment algorithm to optimize shared memory programs with atomic sections a high productivity synchronization construct that enforces atomicity and mutual exclusion Taking a program annotated with atomic sections the compiler automatically infers an assignment of the minimum number of locks to atomic sections so as to exploit the parallelism among atomic sections without violating their semantics Two additional lock optimization problems that are closely related to the lock assignment problem are presented

Distributed Synchronization of Parallel Programs: why and How? Institut National de Recherche en Informatique et en Automatique, P. Quinton, Jean-Pierre Verjus, 1986

Parameterized Verification of Synchronized Concurrent Programs Zeinab Ganjei, 2021-03-19 There is currently an increasing demand for concurrent programs Checking the correctness of concurrent programs is a complex task due to the interleavings of processes Sometimes violation of the correctness properties in such systems causes human or resource losses therefore it is crucial to check the correctness of such systems Two main approaches to software analysis are testing and formal verification Testing can help discover many bugs at a low cost However it cannot prove the correctness of a program Formal verification on the other hand is the approach for proving program correctness Model checking is a formal verification technique that is suitable for concurrent programs It aims to automatically establish the correctness expressed in

terms of temporal properties of a program through an exhaustive search of the behavior of the system Model checking was initially introduced for the purpose of verifying finite state concurrent programs and extending it to infinite state systems is an active research area In this thesis we focus on the formal verification of parameterized systems That is systems in which the number of executing processes is not bounded a priori We provide fully automatic and parameterized model checking techniques for establishing the correctness of safety properties for certain classes of concurrent programs We provide an open source prototype for every technique and present our experimental results on several benchmarks First we address the problem of automatically checking safety properties for bounded as well as parameterized phaser programs Phaser programs are concurrent programs that make use of the complex synchronization construct of Habanero Java phasers For the bounded case we establish the decidability of checking the violation of program assertions and the undecidability of checking deadlock freedom For the parameterized case we study different formulations of the verification problem and propose an exact procedure that is guaranteed to terminate for some reachability problems even in the presence of unbounded phases and arbitrarily many spawned processes Second we propose an approach for automatic verification of parameterized concurrent programs in which shared variables are manipulated by atomic transitions to count and synchronize the spawned processes For this purpose we introduce counting predicates that related counters that refer to the number of processes satisfying some given properties to the variables that are directly manipulated by the concurrent processes We then combine existing works on the counter predicate and constrained monotonic abstraction and build a nested counterexample based refinement scheme to establish correctness Third we introduce Lazy Constrained Monotonic Abstraction for more efficient exploration of well structured abstractions of infinite state non monotonic systems We propose several heuristics and assess the efficiency of the proposed technique by extensive experiments using our open source prototype Lastly we propose a sound but in general incomplete procedure for automatic verification of safety properties for a class of fault tolerant distributed protocols described in the Heard Of HO for short model The HO model is a popular model for describing distributed protocols We propose a verification procedure that is guaranteed to terminate even for unbounded number of the processes that execute the distributed protocol

On Synchronization Patterns in Parallel Programs Jean-Loup Baer, Richard N. Zucker, 1991

Abstract Efficient synchronization is a key element in obtaining good speed up from parallel programs The overhead introduced by synchronization especially lock manipulation can sometimes remove any benefit from parallelizing programs Techniques to efficiently obtain locks under high contention have been studied in the literature using artificial programs We consider the impact of these techniques in a more realistic framework using a sample of real parallel programs running on a shared bus multiprocessor system Cycles lost to lock contention and the number of processors waiting to acquire a lock are the two principal metrics that we use Trace driven simulation experiments are performed for sequentially consistent and weakly consistent architectural models

OpenMP Shared Memory Parallel Programming Rudolf Eigenmann, Michael J.

Voss,2003-05-15 This book contains the presentations given at the Workshop on OpenMP Applications and Tools WOMPAT 2001 The workshop was held on July 30 and 31 2001 at Purdue University West Lafayette Indiana USA It brought together designers users and researchers of the OpenMP application programming interface OpenMP has emerged as the standard for shared memory parallel programming For the first time it is possible to write parallel programs that are portable across the majority of shared memory parallel computers WOMPAT 2001 served as a forum for all those interested in OpenMP and allowed them to meet share ideas and experiences and discuss the latest developments of OpenMP and its applications WOMPAT 2001 was co sponsored by the OpenMP Architecture Review Board ARB It followed a series of workshops on OpenMP including WOMPAT 2000 EWOMP 2000 and WOMPEI 2000 For WOMPAT 2001 we solicited papers formally and published them in the form of this book The authors submitted extended abstracts which were reviewed by the program committee All submitted papers were accepted The authors were asked to prepare a final paper in which they addressed the reviewers comments The proceedings in the form of this book were created in time to be available at the workshop In this way we hope to have brought out a timely report of ongoing OpenMP related research and development efforts as well as ideas for future improvements

Reducing Synchronization in Distributed Parallel Programs Philip B. Miller,2016

Synchronization Costs in Parallel Programs and Concurrent Data Structures Vitalii Aksenov,2018 To use the computational power of modern computing machines we have to deal with concurrent programs Writing efficient concurrent programs is notoriously difficult primarily due to the need of harnessing synchronization costs In this thesis we focus on synchronization costs in parallel programs and concurrent data structures First we present a novel granularity control technique for parallel programs designed for the dynamic multithreading environment Then in the context of concurrent data structures we consider the notion of concurrency optimality and propose the first implementation of a concurrency optimal binary search tree that intuitively accepts a concurrent schedule if and only if the schedule is correct Also we propose parallel combining a technique that enables efficient implementations of concurrent data structures from their parallel batched counterparts We validate the proposed techniques via experimental evaluations showing superior or comparable performance with respect to state of the art algorithms From a more formal perspective we consider the phenomenon of helping in concurrent data structures Intuitively helping is observed when the order of some operation in a linearization is fixed by a step of another process We show that no wait free linearizable implementation of stack using read write compare swap and fetch add primitives can be help free correcting a mistake in an earlier proof by Censor Hillel et al Finally we propose a simple way to analytically predict the throughput of data structures based on coarse grained locking

Synchronization Architecture in Parallel Programming Models Arturo González Escribano,2003 *Analysis of Synchronization in a Parallel Programming Environment* Rice University. Dept. of Computer Science, J. S. Subhlok,1990

Synchronization and Control of Distributed Systems and Programs Michel Raynal,Jean-Michel Helary,1990-10-15 The

mastery of distributed applications demands a mastery of the foundations of the distributed algorithm The object of this book is to present such foundations as far as synchronization the key element of parallelism and distribution is concerned

Transactional Memory. Foundations, Algorithms, Tools, and Applications Rachid Guerraoui, Paolo Romano, 2014-12-29 The advent of multi core architectures and cloud computing has brought parallel programming into the mainstream of software development Unfortunately writing scalable parallel programs using traditional lock based synchronization primitives is well known to be a hard time consuming and error prone task mastered by only a minority of specialized programmers Building on the familiar abstraction of atomic transactions Transactional Memory TM promises to free programmers from the complexity of conventional synchronization schemes simplifying the development and verification of concurrent programs enhancing code reliability and boosting productivity Over the last decade TM has been subject to intense research on a broad range of aspects including hardware and operating systems support language integration as well as algorithms and theoretical foundations On the industrial side the major players of the software and hardware markets have been up front in the research and development of prototypal products providing support for TM systems This has recently led to the introduction of hardware TM implementations on mainstream commercial microprocessors and to the integration of TM support for the world s leading open source compiler In such a vast inter disciplinary domain the Euro TM COST Action IC1001 has served as a catalyzer and a bridge for the various research communities looking at disparate yet subtly interconnected aspects of TM This book emerged from the idea having Euro TM experts compile recent results in the TM area in a single and consistent volume Contributions have been carefully selected and revised to provide a broad coverage of several fundamental issues associated with the design and implementation of TM systems including their theoretical underpinnings and algorithmic foundations programming language integration and verification tools hardware supports distributed TM systems self tuning mechanisms as well as lessons learnt from building complex TM based applications

A New Construct for Synchronization of Parallel Logic Programs University of Maryland at College Park. Computer Science Center, S. Kasif, 1985

OpenMP in the Era of Low Power Devices and Accelerators Alistair P. Rendell, Barbara M. Chapman, Matthias S. Müller, 2013-08-15 This book constitutes the refereed proceedings of the 9th International Workshop on OpenMP held in Canberra Australia in September 2013 The 14 technical full papers presented were carefully reviewed and selected from various submissions The papers are organized in topical sections on proposed extensions to OpenMP applications accelerators scheduling and tools

Languages and Compilers for Parallel Computing, 1993

Synchronization Expressions in Parallel Programming Languages Lifu Guo, 1994

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